

# International Rectifier

- Advanced Process Technology
- Surface Mount (IRFZ48NS)
- Low-profile through-hole (IRFZ48NL)
- 175°C Operating Temperature
- Fast Switching
- Fully Avalanche Rated
- Lead-Free

## Description

Advanced HEXFET® Power MOSFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that HEXFET power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in a wide variety of applications.

The D<sup>2</sup>Pak is a surface mount power package capable of accommodating die sizes up to HEX-4. It provides the highest power capability and the lowest possible on-resistance in any existing surface mount package. The D<sup>2</sup>Pak is suitable for high current applications because of its low internal connection resistance and can dissipate up to 2.0W in a typical surface mount application.

The through-hole version (IRFZ48NL) is available for low-profile applications.

## Absolute Maximum Ratings

	Parameter	Max.	Units
I <sub>D</sub> @ T <sub>C</sub> = 25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V	64	A
I <sub>D</sub> @ T <sub>C</sub> = 100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V	45	
I <sub>DM</sub>	Pulsed Drain Current ①	210	
P <sub>D</sub> @ T <sub>A</sub> = 25°C	Power Dissipation	3.8	W
P <sub>D</sub> @ T <sub>C</sub> = 25°C	Power Dissipation	130	W
	Linear Derating Factor	0.83	W/°C
V <sub>GS</sub>	Gate-to-Source Voltage	± 20	V
I <sub>AR</sub>	Avalanche Current ②	32	A
E <sub>AR</sub>	Repetitive Avalanche Energy ③	13	mJ
dv/dt	Peak Diode Recovery dv/dt ④	5.0	V/ns
T <sub>J</sub>	Operating Junction and	-55 to + 175	°C
T <sub>STG</sub>	Storage Temperature Range Soldering Temperature, for 10 seconds	300 (1.6mm from case )	

## Thermal Resistance

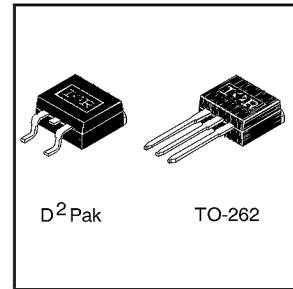
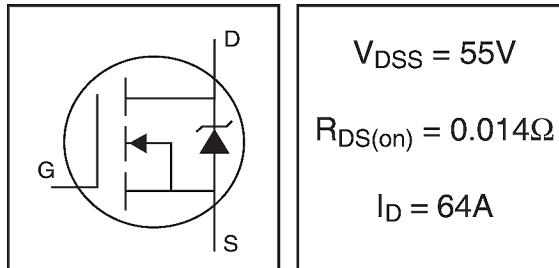
	Parameter	Typ.	Max.	Units
R <sub>QJC</sub>	Junction-to-Case	—	1.15	°C/W
R <sub>QJA</sub>	Junction-to-Ambient ( PCB Mounted,steady-state)*	—	40	

PD - 95125

**IRFZ48NSPbF**

**IRFZ48NLPbF**

HEXFET® Power MOSFET



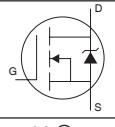
# IRFZ48NS/LPbF

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## Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(\text{BR})\text{DSS}}$	Drain-to-Source Breakdown Voltage	55	—	—	V	$V_{\text{GS}} = 0\text{V}$ , $I_D = 250\mu\text{A}$
$\Delta V_{(\text{BR})\text{DSS}/\Delta T_J}$	Breakdown Voltage Temp. Coefficient	—	0.058	—	V/ $^\circ\text{C}$	Reference to $25^\circ\text{C}$ , $I_D = 1\text{mA}$
$R_{\text{DS}(\text{on})}$	Static Drain-to-Source On-Resistance	—	—	14	$\text{m}\Omega$	$V_{\text{GS}} = 10\text{V}$ , $I_D = 32\text{A}$ ④
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	2.0	—	4.0	V	$V_{\text{DS}} = V_{\text{GS}}$ , $I_D = 250\mu\text{A}$
$g_{\text{fs}}$	Forward Transconductance	24	—	—	S	$V_{\text{DS}} = 25\text{V}$ , $I_D = 32\text{A}$ ④
$I_{\text{DSS}}$	Drain-to-Source Leakage Current	—	—	25	$\mu\text{A}$	$V_{\text{DS}} = 55\text{V}$ , $V_{\text{GS}} = 0\text{V}$
		—	—	250		$V_{\text{DS}} = 44\text{V}$ , $V_{\text{GS}} = 0\text{V}$ , $T_J = 150^\circ\text{C}$
$I_{\text{GSS}}$	Gate-to-Source Forward Leakage	—	—	100	nA	$V_{\text{GS}} = 20\text{V}$
	Gate-to-Source Reverse Leakage	—	—	-100		$V_{\text{GS}} = -20\text{V}$
$Q_g$	Total Gate Charge	—	—	81	nC	$I_D = 32\text{A}$
$Q_{\text{gs}}$	Gate-to-Source Charge	—	—	19		$V_{\text{DS}} = 44\text{V}$
$Q_{\text{gd}}$	Gate-to-Drain ("Miller") Charge	—	—	30		$V_{\text{GS}} = 10\text{V}$ , See Fig. 6 and 13
$t_{\text{d}(\text{on})}$	Turn-On Delay Time	—	12	—	ns	$V_{\text{DD}} = 28\text{V}$
$t_r$	Rise Time	—	78	—		$I_D = 32\text{A}$
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time	—	34	—		$R_G = 0.85\Omega$
$t_f$	Fall Time	—	50	—		$V_{\text{GS}} = 10\text{V}$ , See Fig. 10 ④
$L_S$	Internal Source Inductance	—	7.5	—	nH	Between lead, and center of die contact
$C_{\text{iss}}$	Input Capacitance	—	1970	—	pF	$V_{\text{GS}} = 0\text{V}$
$C_{\text{oss}}$	Output Capacitance	—	470	—		$V_{\text{DS}} = 25\text{V}$
$C_{\text{rss}}$	Reverse Transfer Capacitance	—	120	—		$f = 1.0\text{MHz}$ , See Fig. 5
$E_{\text{AS}}$	Single Pulse Avalanche Energy ②	—	700 ⑤	190 ⑥	mJ	$I_{\text{AS}} = 32\text{A}$ , $L = 0.37\text{mH}$

## Source-Drain Ratings and Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
$I_S$	Continuous Source Current (Body Diode)	—	—	64	A	MOSFET symbol showing the integral reverse p-n junction diode. 
$I_{\text{SM}}$	Pulsed Source Current (Body Diode) ①	—	—	210		
$V_{\text{SD}}$	Diode Forward Voltage	—	—	1.3	V	$T_J = 25^\circ\text{C}$ , $I_S = 32\text{A}$ , $V_{\text{GS}} = 0\text{V}$ ④
$t_{rr}$	Reverse Recovery Time	—	68	100	ns	$T_J = 25^\circ\text{C}$ , $I_F = 32\text{A}$
$Q_{rr}$	Reverse Recovery Charge	—	220	330	nC	$di/dt = 100\text{A}/\mu\text{s}$ ④
$t_{\text{on}}$	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by $L_S+L_D$ )				

### Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature. ( See fig. 11 )
- ② Starting  $T_J = 25^\circ\text{C}$ ,  $L = 0.37\text{mH}$   
 $R_G = 25\Omega$ ,  $I_{\text{AS}} = 32\text{A}$ . (See Figure 12)
- ③  $I_{SD} \leq 32\text{A}$ ,  $di/dt \leq 220\text{A}/\mu\text{s}$ ,  $V_{\text{DD}} \leq V_{(\text{BR})\text{DSS}}$ ,  
 $T_J \leq 175^\circ\text{C}$
- ④ Pulse width  $\leq 400\mu\text{s}$ ; duty cycle  $\leq 2\%$ .
- ⑤ This is the destructive value not limited to the thermal limit.
- ⑥ This is the thermal limited value.

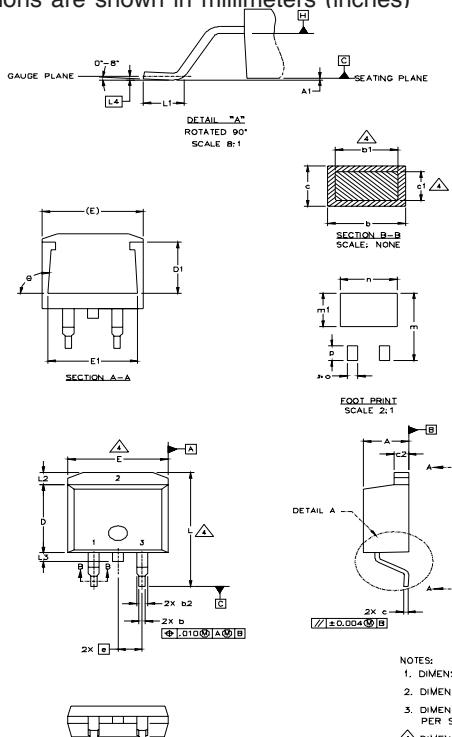
\*\* When mounted on 1" square PCB ( FR-4 or G-10 Material ).  
For recommended soldering techniques refer to application note #AN-994.

# IRFZ48NS/LPbF

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## D<sup>2</sup>Pak Package Outline

Dimensions are shown in millimeters (inches)



SYMBOL	DIMENSIONS				NOTES	
	MILLIMETERS		INCHES			
	MIN.	MAX.	MIN.	MAX.		
A	4.06	4.83	.160	.190		
A1		0.127		.005		
b	0.51	0.99	.020	.039	4	
b1	0.51	0.89	.020	.035		
b2	1.14	1.40	.045	.055		
c	0.43	0.63	.017	.025		
c1	0.38	0.74	.015	.029	4	
c2	1.14	1.40	.045	.055		
D	8.51	9.65	.335	.380	3	
D1	5.33		.210			
E	9.65	10.67	.380	.420	3	
E1	6.22		.245			
e	2.54	BSC	.100	BSC		
L	14.61	15.88	.575	.625		
L1	1.78	2.79	.070	.110		
L2		1.65		.065		
L3	1.27	1.78	.050	.070		
L4	0.25	BSC	.010	BSC		
m	17.78		.700			
m1	8.89		.350			
n	11.43		.450			
o	2.08		.082			
p	3.81		.150			
θ	90°	93°	90°	93°		

### LEAD ASSIGNMENTS

HEXFET	IGBTs, CoPACK	DIODES
1.— GATE	1.— GATE	1.— ANODE *
2.— DRAIN	2.— COLLECTOR	2.— CATHODE
3.— SOURCE	3.— Emitter	3.— ANODE

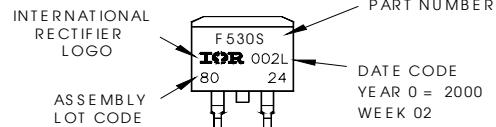
\* PART DEPENDENT.

- NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994
  2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES]
  3. DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 [ .005"] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY.
  4. DIMENSION b1 AND c1 APPLY TO BASE METAL ONLY.
  5. CONTROLLING DIMENSION: INCH.

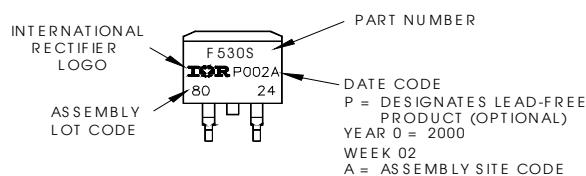
## D<sup>2</sup>Pak Part Marking Information (Lead-Free)

EXAMPLE: THIS IS AN IRF530S WITH  
LOT CODE 8024  
ASSEMBLED ON WW 02, 2000  
IN THE ASSEMBLY LINE "L"

Note: "P" in assembly line  
position indicates "Lead-Free"

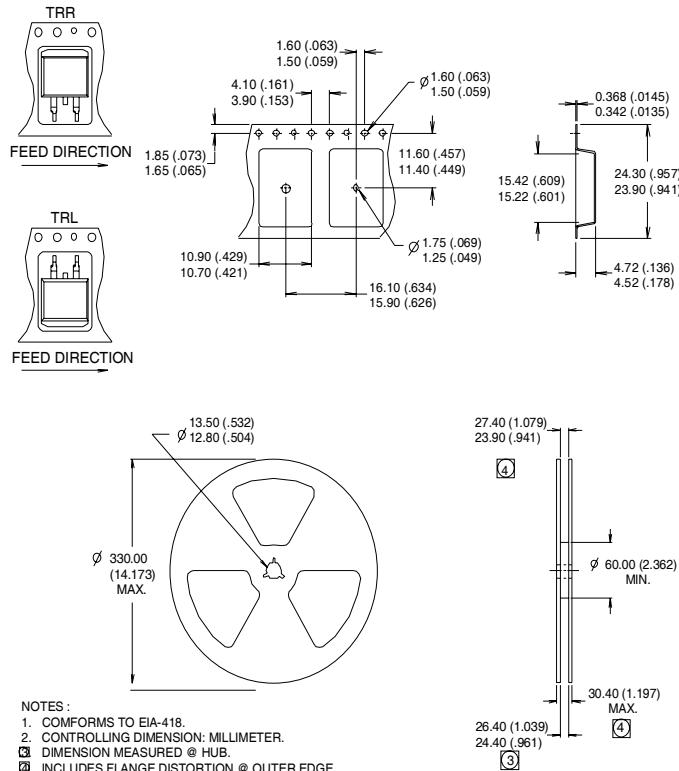


OR



## D<sup>2</sup>Pak Tape & Reel Infomation

Dimensions are shown in millimeters (inches)



Data and specifications subject to change without notice.  
 This product has been designed and qualified for the industrial market.  
 Qualification Standards can be found on IR's Web site.

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